



RAN - 2003000204020092

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**S.Y.B.Sc. (Sem. - IV) Examination April - 2025**

**Electronics : Paper - IV (Old)**

**Microprocessor 8085**

[ Total Marks: 50

**सूचना : / Instructions**

(१)

नीचे दशावलि निशानीवाणी विगतो उत्तरवही पर अवश्य लपवी.  
Fill up strictly the details of signs on your answer book

Name of the Examination:

S.Y.B.Sc. (Sem. - IV)

Name of the Subject :

Electronics IV (Old) Microprocessor 8085

Subject Code No.: 2003000204020092

Seat No.:

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Student's Signature

- (2) Figure on the right indicates full marks
- (3) All symbols and abbreviations have their usual meaning.
- (4) Non-programmable calculators are allowing.
- (5) Q.1 is compulsory.
- (6) Assume data, if necessary

**Q.1 Answer in short:**

**08**

- (a) What is a stack top?
- (b) What is a microprocessor?
- (c) Explain the instructions MOV A,B and MVI A,05H
- (d) What do you understand by vector interrupt?

**Q.2 (a)** Explain the function of the following pins of 8085 microprocessor

(i) TRAP

(ii) HLDA

(iii) RESET OUT

(iv) READY

**08**

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- (b) What is the software interrupts of 8085? Mention the instructions, their hex codes and the corresponding vector addresses. **06**
- OR**
- Q.2** (a) Draw the functional block diagram of 8085 microprocessor and explain in detail the function of various registers. **08**
- (b) write a program to add two numbers given in register B and C and store the result in register A **06**
- Q.3** (a) Explain the different instructions of data transfer group of 8085 microprocessor **08**
- (b) Write a program to add two data bytes. Data are stored in memory locations 7400H and 7401H. The result is 8 bits only. **06**
- OR**
- Q.3** (a) State various addressing modes in 8085  $\mu$ P and explain any two of them in detail, **08**
- (b) Discuss the two registers program counter and stack pointer. **06**
- Q.4** **Write short note on (Any TWO)** **14**
- (a) PUSH and POP Instructions
- (b) Branch group instruction set
- (c) Subroutine
- (d) Differentiate between normal I/O and memory mapped I/O
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